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Display panel

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Display panel

The invention relates to a display panel formed on a substrate and comprising a plurality of display pixels with at least one light emissive layer and at least one electrode layer deposited on or over said light emissive layer.

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Display panels employing display pixels comprising electroluminescent material on or over a substrate are becoming increasingly popular. These light emitting elements may be light emitting diodes (LED's), incorporated in or forming the display pixels that are arranged in a matrix of rows and columns. The materials employed in such LED's are suitable to generate light if a current is conveyed through these materials, such as particular polymeric (PLED) or small molecule organic (SMOLED) materials. Accordingly the LED's have to be arranged such that a flow of current can be driven through these electroluminescent materials. Typically passively and actively driven matrix displays are distinguished. For active matrix displays, the display pixels themselves comprise active circuitry such as one or more transistors.

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PLED materials provide advantages over SMOLED materials due to their intrinsic characteristics of thermal stability, flexibility and solubility in aqueous solutions or solvents. As a result PLED materials can be applied by wet chemical techniques such as spincoating or ink jet deposition.

20

EP-A-0 892 028 discloses an organic EL element wherein transparent pixel electrodes are formed on a transparent substrate. Photolithographically defined photoresist banks are formed between the pixel electrodes to prevent a liquid ink drop comprising electroluminescent material to unintentionally flow to adjacent display pixels.

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In particular display panels, e.g. those having top emission or translucent display panels, the top electrode layer for applying the current to the electroluminescent materials should be transparent for the light emitted from the display pixels. Such transparent electrode layers intrinsically have a relatively high electrical resistance. For such display panel therefore a conflict exists in either increasing the thickness of the electrode layer to decrease the electrical resistivity at the cost of transparency or to decreasing the thickness of

the top electrode layer to enhance the transparency yielding an even higher electrical resistivity having detrimental effects to the power consumption of devices employing such display panels.

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It is an object of the invention to provide a display panel with an optically transparent electrode layer of low resistance.

This object is achieved by providing a display panel that further comprises electrically conductive structures shunting said electrode layer. In shunting, the electrode
10 layer is cross connected by one or more of said electrically conductive structures that are able to divert a part of the electrical current through the electrode layer over these electrically conductive structures. In such display device the electrical resistance of the electrode layer is decreased by an external measure not affecting the thickness of the electrode layer. Therefore the thickness of the electrode layer can be maintained reasonably thin without resulting in an
15 unacceptable increase of the electrical resistance of the electrode layer.

In an embodiment of the invention the display pixels are separated by barrier structures forming said electrically conductive structures and said electrode layer contacts said barrier structures for shunting said electrode layer. As the barrier structures themselves are typically already present between the display pixels, usage of these barrier structures for
20 shunting avoids additional manufacturing steps and the aperture of the display pixels is not reduced by such an electrically conductive structure.

In an embodiment of the invention the barrier structures of adjacent display pixels are in electrical contact. Although not essential, as the electrode layer itself already connects the barrier structures, the electrical contact between the barrier structures may
25 further reduce the resistance of the electrical path resulting in an improved shunting performance.

In an embodiment of the invention an insulation layer separates the light emissive layer from said barrier structures. As the light emissive layer is electrically conductive, this insulation layer avoids the flow of leakage currents between an electrode
30 underneath the light emissive layer and the electrode layer via the barrier structure. Further the chemical activity of particular substances such as a PEDOT hole injection layer may corrode or oxidise the barrier structures resulting in bad electrical contacts with the electrode layer in this area. Preferably the barrier structures comprise side walls covered by a hydrophobic insulating layer, such as a photoresist layer or a a-Si layer as an insulating

spacer layer. Alternatively the barrier structures may comprise side walls having a substantially inclined orientation with respect to said substrate, said side walls being covered by an anodised insulating spacer layer.

In an embodiment of the invention the display panel further comprises structures to locally separate said electrode layer. Such structures may be build-in shadow masks that can be employed to separate the electrode layer in a plurality of strips typically used in passive matrix display panels. These strips are still shunted by the barrier structures of the display pixels of a particular strip.

In an embodiment of the invention the barrier structures are available at or near at least one edge of the display panel, e.g. to contact outside electronics.

In an embodiment of the invention the barrier structures are at least partially covered by at least one light absorbing electrically conductive layer. Such a light absorbing layer is advantageous for top-emission display panels to reduce light scattering of the metallic barrier structures to improve e.g. daylight contrast. Preferably the light absorbing layer comprises an oxide material or an oxide-metal material combination. Such layer may either be intrinsically light absorbent or react with other layers of the display panel, such as the electrode layer to be deposited afterwards, to become or stay light absorbent after deposition.

In an embodiment of the invention the barrier structures are fully reflective or covered with a reflective layer and the display panel further comprises a light blocking layer, e.g. a circular polariser. Any incoming light is entirely reflected by the barrier structure and completely blocked subsequently, such that light scattering is reduced or eliminated.

It should be appreciated that the display panel discussed above may constitute either a part of an electric device product or an electric device product as such. Such an electric device product may e.g. relate to handheld devices such as a mobile phone, a Personal Digital Assistant (PDA) or a portable computer as well as to devices such as a monitor for a Personal Computer, a television set or a display on e.g. a dashboard of a car.

The invention further relates to a method for manufacturing a display panel on a substrate comprising the steps of:

- defining a plurality of display pixel areas by deposition of electrically conductive barrier structures on or over said substrate;
- filling said separated display pixel areas bounded by said barrier structures with at least one substance forming a light emissive layer;

- depositing an electrode layer on or over said light emissive layer and in contact with said barrier structures.

This method results in a display panel wherein the thickness of the electrode layer can be maintained reasonably thin without resulting in an unacceptable increase of the electrical resistance of the electrode layer. As the barrier structures themselves are already present between the display pixels, usage of these barrier structures for shunting avoids additional manufacturing steps and the aperture of the display pixels is not reduced by an additional electrically conductive structure.

In an embodiment of the invention the method further includes the step of forming an insulating spacer layer between said polymer substance and said barrier structure. Such an insulating layer may reduce or eliminate leakage currents and avoids chemical degradation of the barrier structures.

In a preferred embodiment of the invention the method further includes the steps of:

- having available a mask layer on or over said barrier structures;
- underetching said mask layer to form substantially inclined side walls for said barrier structures;
- depositing an oxide insulating spacer layer by executing an anodisation treatment using a counter electrode and connecting said electrically conductive barrier structures as a second electrode in an anodisation bath.

These steps were found to be advantageous as the electrically conductive barrier structures can be made available for external connection, such that the barrier structures may constitute an electrode in an anodisation bath. Surprisingly water was found as an ideal liquid to perform the anodisation treatment using a stack of TiW and Al for the barrier structure and Al as the first electrode.

The invention will be further illustrated with reference to the attached drawings, which show a preferred embodiment according to the invention. It will be understood that the invention is not in any way restricted to these specific and preferred embodiments.

In the drawings:

Fig. 1 shows a schematical electric device with a display panel according to an embodiment of the invention;

Fig. 2 shows a part of a display panel according to a first embodiment of the invention

Fig. 3 shows a cross-section along I-I of Fig. 2 ;

Fig. 4 shows a cross-section of a part of a display panel according to a second
5 embodiment of the invention;

Fig. 5 shows a cross-section of a part of a display panel according to a third embodiment of the invention;

Fig. 6 shows a cross-section of a part of a display panel during manufacturing of the display panel of Fig. 5;

Fig. 7 illustrates an anodisation treatment performed on the display panel shown
10 in Fig. 6.

Fig. 8 shows a cross-section of a part of a display panel according to a fourth embodiment of the invention, and

Fig. 9 shows a cross-section of a part of a display panel according to a fifth
15 embodiment of the invention.

Fig. 1 schematically shows a display panel 1 of an electric device 2 having a plurality of display pixels 3 arranged in a matrix of rows and columns. The display panel 2
20 may either be an active matrix display panel or a passive matrix display panel 1 containing polymer light emitting diodes (PLEDs) or small molecule light emitting diodes (SMOLEDs)

Fig. 2 illustrates a part of the display panel 1 with three display pixels 3 according to a first embodiment of the invention. Fig. 3 shows a cross-section along line I-I of the part of the display panel 1 shown in Fig. 2.

25 A first electrode layer 4, commonly referred to as the anode, is deposited on or over the substrate 5, e.g. by vacuum evaporation or sputtering. The anode 4 can subsequently be patterned by photolithography as shown in Fig. 2 by the dashed areas. The anode 4 may either be transparent, absorbent or reflective, depending on the type of emission required for the display panel 1. Generally the anode 4 is transparent for a bottom-emission type of
30 display panel 1, wherein light is emitted through the substrate 5 or for a translucent display panel 1, wherein the display panel 1 is fully transparent. For example, a transparent hole-injecting electrode material, such as Indium-Tin-Oxide (ITO), may be used. The anode 4 is usually reflective for a top-emission type of display panel 1, wherein light is emitted from the

display panel 1 away from the substrate 5. In this case the anode 4 may be a metal or a metal covered with ITO.

After patterning of the anode 4, an insulating layer 6 of e.g. silicon-dioxide and one or more electrically conductive layers, e.g. aluminium and molybdenum layers are deposited. The electrically conductive layers are subsequently patterned to define electrically conductive structures 7. In this embodiment the electrically conductive structures 7 are used as barrier structures 7 that define or separate adjacent display pixels 3. Further the SiO₂-layer 6 is defined to expose the anode 4 at a part of the display pixel area A and holes may be defined for electrical cross-connections, if required. Preferably the barrier structures 7 are in electrical contact, as shown in Fig. 2. The thickness of the barrier structures typically vary in the range of 0,1-5µm, more preferably in the range of 0,5-3µm, such as 1,5µm.

For passive matrix display panels 1, and occasionally as well for active matrix display panels 1, further structures 8 can be applied.

Subsequently polymers 9 are deposited, e.g. by inkjet printing. It is noted that a display pixel 3 may comprise several conductive polymer layers, such as a polyethylenedioxythiophene (PEDOT) layer and a polyphenylenevinylene (PPV), the latter being a light emitting polymer (LEP). For a colour light emitting display different light emitting materials may be used. The metallic barrier structure 7 typically is hydrophobic, while the SiO₂-layer 6 is hydrophilic. Therefore the light emitting materials spread appropriately over the display pixel area A.

Finally an electrode layer 10, commonly referred to as cathode, is deposited over the display panel. For clarity purposes this cathode 10 is not shown in Fig. 2. Typically for active matrix display panels 1 the cathode is a non-interrupted slab, i.e. a common cathode is used for all display pixels 3, while for passive matrix display panels 1 the cathode is separated for each row or column by the further structure 8. The cathode 10 may be either transparent or reflective. Especially for a transparent cathode 10 a high electrical resistivity is present. The cathode 10 is shunted via the electrically conductive barrier structures 7, such that a considerable reduction of the thickness of the cathode 10 can be achieved. Preferably the thickness of the cathode 10 is in the range of 10-100 nm, and more preferably in the range of 10-80nm, such as 30nm. This considerably reduced thickness significantly enhances the transparency of the cathode. The barrier structures 7 may be metallic or comprise metallic parts enabling conduction of currents of the cathode 10. It is noted that the walls of the barrier structures 7 may be steep as the cathode 10 is at least in electrical contact with the top surface of the barrier structures 7. However, preferably the walls of the barrier structures 7

have an inclined orientation to the substrate 5, as shown e.g. in Fig. 5, to obtain a shallow barrier structure allowing a good step coverage by the cathode layer 10.

In the case of a passive matrix display panel 1, the cathode 10 is structured by the further structures 8 in lines whereas the shunting by the barrier structure 7 is present for each particular line.

The deposition of the polymer substances in the display pixel area A frequently results in contact between the PEDOT and the metallic barrier structure 7. As PEDOT is electrically conductive a leakage current is likely to flow between the anode 4 and the cathode 10 via the electrically conductive structure 7. Further, as the PEDOT-solution is acidic, the aluminium of the barrier structure 7 may be affected detrimentally causing local corrosion. This may eventually result in bad contact of the barrier structure 7 and the cathode 10.

The embodiments shown in Figs. 4 and 5 take these effects into account by providing an insulating layer 11 that separates the polymers 9 from the barrier structures 7.

In Fig. 4 the barrier structures 7 comprise side walls being covered by a hydrophobic insulating layer, such as amorphous silicon or photoresists, as an insulating spacer layer 11. The spacer layer 11 is preferably hydrophobic or made hydrophobic before polymer processing such that the spacer layer 11 is not be wetted by the polymers 9.

In this embodiment a photoresist is deposited over the display panel, e.g. by spincoating. In case the further structures 8 should be applied, this step is preferably taken before definition of these further structures 8. The photoresist layer preferably is deposited to a thickness not much larger than the height of the barrier structures 7 to avoid planarisation of the photoresist over the display panel 1. After deposition of the resist an anisotropic RIE-etch is performed with an O₂-plasma, such that at the side walls of the barrier structures 7 the insulating spacer layers 11 are formed. The spacer layer 11 is smooth such that the cathode 10 (not shown in Fig. 4) can be deposited over the structure without interruptions. The spacer layer 11 protects the metallic barrier structure 7 from the polymers 9, especially the PEDOT, such that the above-mentioned detrimental effects will not or less likely occur.

In Fig. 5 the barrier structures 7 comprise side walls 12 having a substantially inclined orientation with respect to the substrate 5. The side walls 12 are covered by an anodised insulating spacer layer 11. The insulating spacer layer 11 again protects the metallic barrier structure 7 from being detrimentally effected by the PEDOT-material.

Fig. 6 and 7 illustrate a preferred manufacturing process for the embodiment displayed in Fig. 5. After definition of the metallic barrier structures 7 the inclined side walls

12 are formed by underetching the photoresist mask 13 used for defining the barrier structures 7 either by wet etching or plasma etching. The photoresist mask 13 is maintained to be used in a subsequent anodisation treatment for applying the insulating spacer layer 11 on the inclined side walls 12. The metallic barrier structures 7 are available on the display panel 1 for electrical connection with a contact element 14. If the barrier structure comprises TiW and Al, it was observed that water 15 can be used as an oxidiser for application of the insulating spacer layer 11 on the inclined side walls 12. A voltage source 16 applying 30 V between the contact element 14 and an Al-counter electrode 17 was found to result in an AlO-spacer layer 11. After the anodisation treatment, the photoresist mask 13 is removed. If required, the further structures 8 can be defined on the display panel 1 afterwards. The contact element 14 can be removed from the display panel 1 as this element 14 shortens the respective barrier structures 7. However, for an active matrix display panel 1 with a common cathode 10, the element 14 may be maintained to contact the cathode 10.

It is noted that the described embodiments may all include further layers or structures and are generally protected by a lid.

In conclusion the invention allows the cathode 10 to combine appropriate transparency with a reasonably low electrical resistivity by electrically shunting of the cathode 10, preferably by using the barrier structures 7 between the display pixels 3.

Fig. 8 schematically shows an embodiment of the invention for a top-emission display panel 1, wherein the barrier structures 7 are covered by at least one light absorbing electrically conductive layer 18, hereinafter also referred to as black layer 18. The black layer 18 may be deposited on top of the electrically conductive layer before structuring this layer to obtain the barrier structures 7. The black layer 18 may comprise an oxidic material having some conductance, such as RuO₂, IrO₂, Pb₂Ru₂O₇, Bi₂Ru₂O₇ or Pb₃Rh₇O₁₅. Such layers 18 do not react with the material of the cathode 10 and are intrinsically absorbent and conductive. Another class of materials can be those, which will react with the cathode 10, but have some resistance to the further processing such as the printing of the light emitting polymers, the deposition of the cathode 10 and the encapsulation. After reaction with the cathode 10 these materials should become absorbent or stay absorbent and conductive. Materials may include mixtures of SiO with Al, SiO with Ag or other oxide-metal combinations. The black layer 18 preferably has a limited thickness, such as in the range of 20-500nm, such as 100nm.

Fig. 9 schematically shows an embodiment of the invention wherein the barrier structures 7 are fully reflective or covered with a reflective layer 19 and the display

panel 1 further comprises a contrast enhancement layer 20, e.g. a circular polariser. The reflective layer 19 may be added to increase the reflectivity. Preferably the reflective layer 19 is thin, such as in the range of 20-100nm, to obtain a highly reflective layer. The contrast enhancement layer 20 is provided on top of the display panel 1, that is over the protective structure 21. Any incoming light is entirely reflected by the barrier structure 7 or the reflective layer 19 and completely blocked subsequently by the light blocking layer 20, such that light scattering from incident daylight is reduced or eliminated.

CLAIMS:

1. Display panel (1) formed on a substrate (5) and comprising a plurality of display pixels (3) with at least one light emissive layer (9) and at least one electrode layer (10) deposited on or over said light emissive layer (9), wherein said display panel (1) further comprises electrically conductive structures (7) shunting said electrode layer (10).

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2. Display panel (1) according to claim 1, wherein said display pixels (3) are separated by barrier structures forming said electrically conductive structures (7) and said electrode layer (10) contacts said barrier structures for shunting said electrode layer (10).

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3. Display panel (1) according to claim 2, wherein said barrier structures (7) of adjacent display pixels (3) are in electrical contact.

4. Display panel (1) according to claim 2, wherein at least one insulation layer (11) separates said light emissive layer (9) from said barrier structures (7).

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5. Display panel (1) according to claim 2, wherein said barrier structures (7) comprise side walls being covered by a hydrophobic insulation layer (11), such as an amorphous silicon layer or a photoresist layer as an insulating spacer layer (11).

20

6. Display panel (1) according to claim 2, wherein said barrier structures (7) comprise side walls (12) having a substantially inclined orientation with respect to said substrate (5), said side walls (12) being covered by an anodised insulating spacer layer (11).

25

7. Display panel (1) according to claim 2, wherein said display panel (1) further comprises structures (8) to locally separate said electrode layer (10).

8. Display panel (1) according to claim 2, wherein said barrier structures (7) are available at or near at least one edge of the display panel (1).

9. Display panel (1) according to claim 2, wherein said barrier structures (7) are at least partially covered by at least one light absorbing electrically conductive layer (18).

10. Display panel (1) according to claim 9, wherein said light absorbing electrically conductive layer (18) comprises an oxide material or an oxide-metal material combination.

11. Display panel (1) according to claim 2, wherein said barrier structures (7) are fully reflective or covered with a reflective layer (19) and said display panel (1) further comprises a polarisation layer (20).

12. Method for manufacturing a display panel (1) on a substrate (5) comprising the steps of:

- defining a plurality of display pixel areas (A) by deposition of electrically conductive barrier structures (7) on or over said substrate (5);
- filling said separated display pixel areas (A) bounded by said barrier structures (7) with at least one substance to form a light emissive layer (9);
- depositing an electrode layer (10) on or over said light emissive layer (9) and in contact with said barrier structures (7).

13. Method for manufacturing a display panel (1) according to claim 12, further including the step of forming an insulating spacer layer (11) between said polymer substance (9) and said barrier structure (7).

14. Method for manufacturing a display panel (1) according to claim 12, further including the steps of:

- having available a mask layer (13) on or over said barrier structures (7);
- underetching said mask layer (13) to form substantially inclined side walls (12) for said barrier structures (7);
- depositing an oxide insulating spacer layer (11) by executing an anodisation treatment using a counter electrode (17) and connecting said electrically conductive barrier structures (7) as a second electrode in an anodisation bath.

15. Method for manufacturing a display panel (1) according to claim 14, wherein said anodisation bath contains water (15) for oxidising said side walls (12).

ABSTRACT:

The invention relates to a display panel (1) formed on a substrate (5) and comprising a plurality of display pixels (3) with at least one light emissive layer (9) and at least one electrode layer (10) or cathode deposited on or over said light emissive layer (9). The display panel (1) further comprises electrically conductive structures (7) shunting said
5 electrode layer (10). Preferably barrier structures separating the display pixels (3) are used as the electrically conductive structures (7). The invention allows the cathode (10) to combine appropriate transparency with a reasonably low electrical resistivity.

Fig. 3

1/4

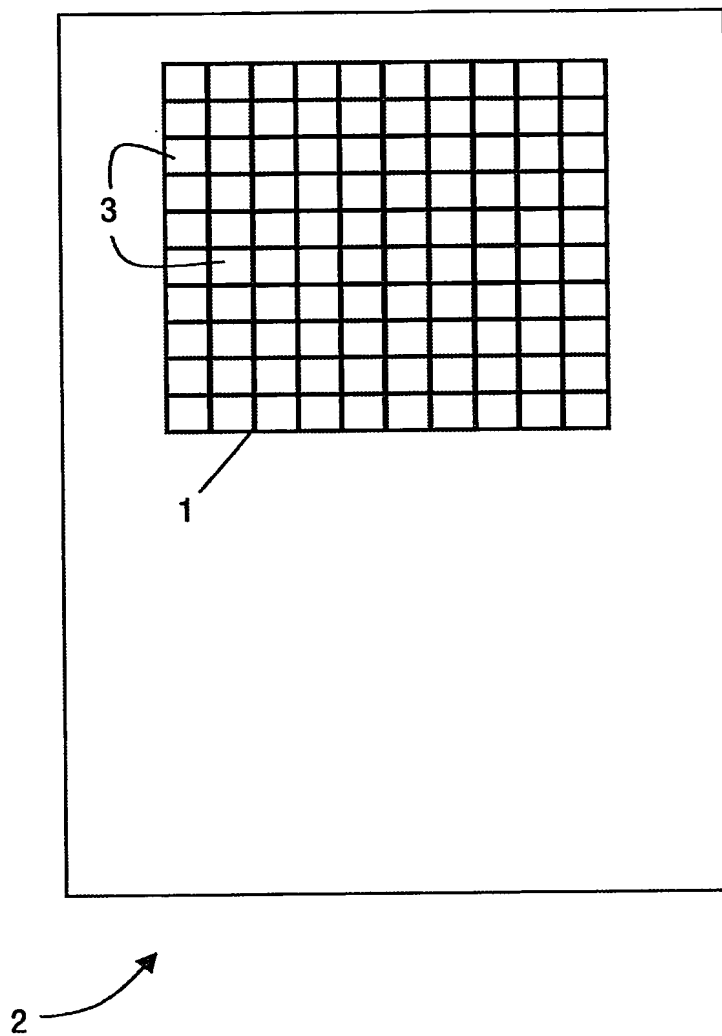


FIG.1

2/4

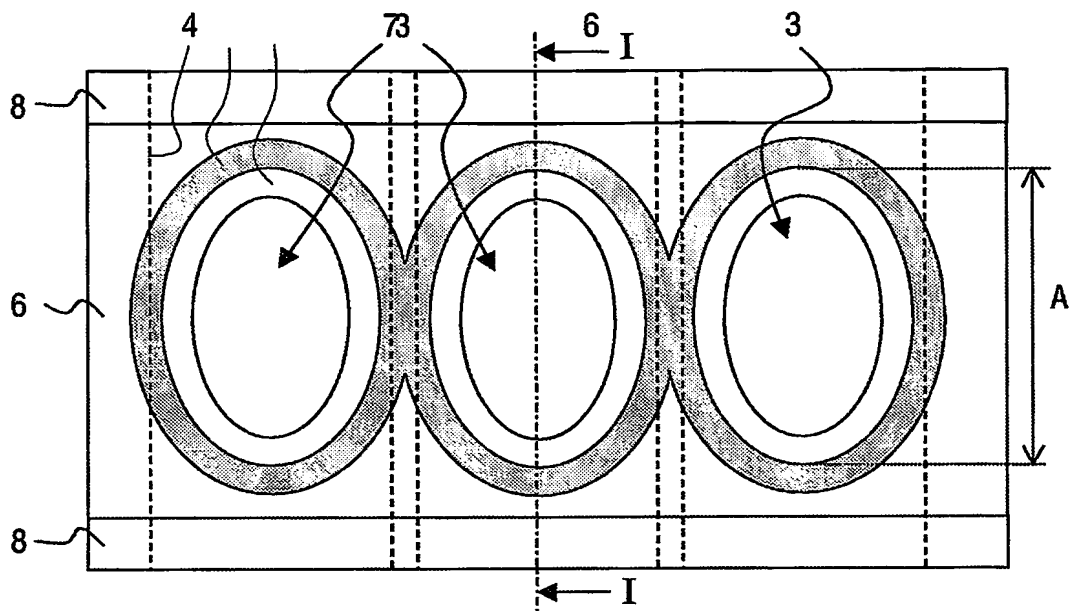


FIG. 2

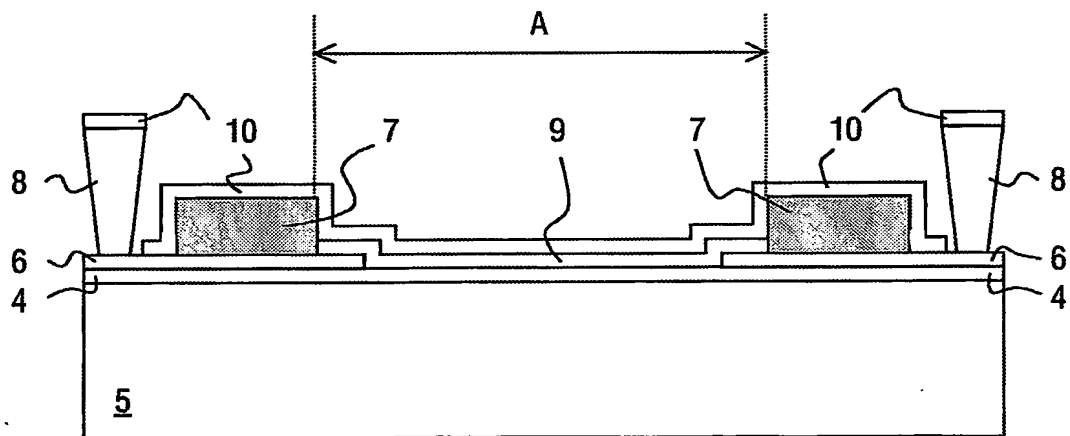


FIG. 3

3/4

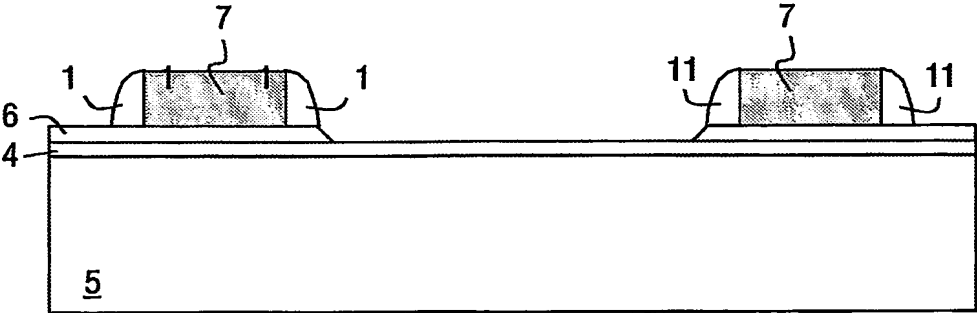


FIG.4

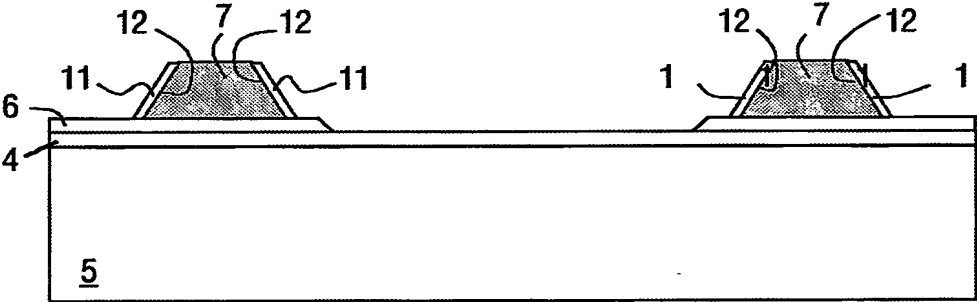


FIG.5

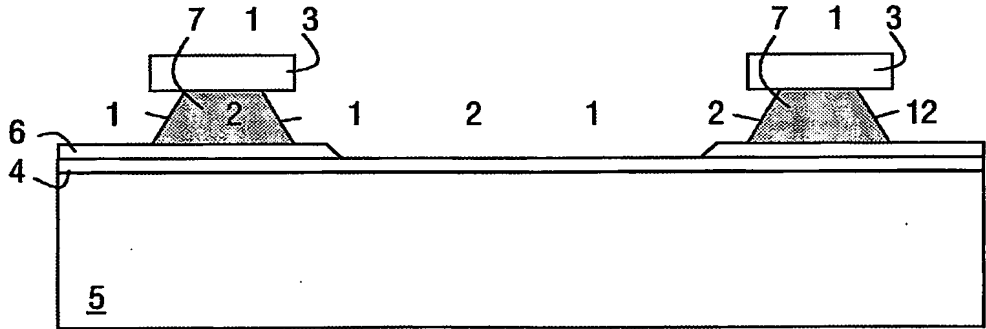


FIG.6

4/4

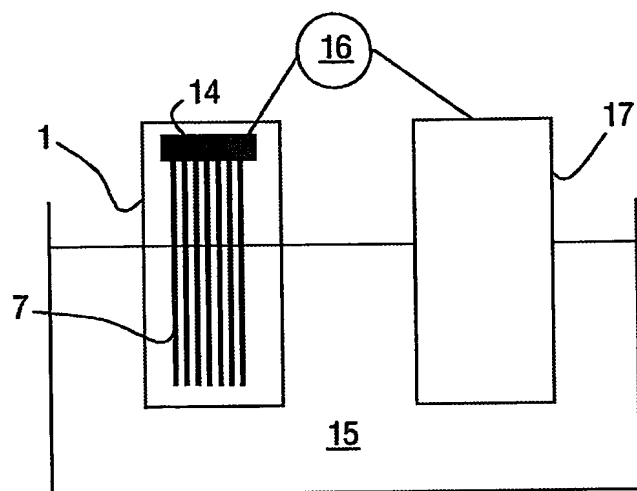


FIG. 7

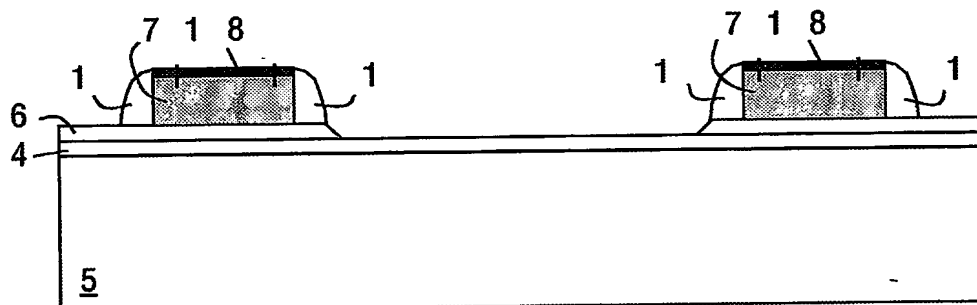


FIG. 8

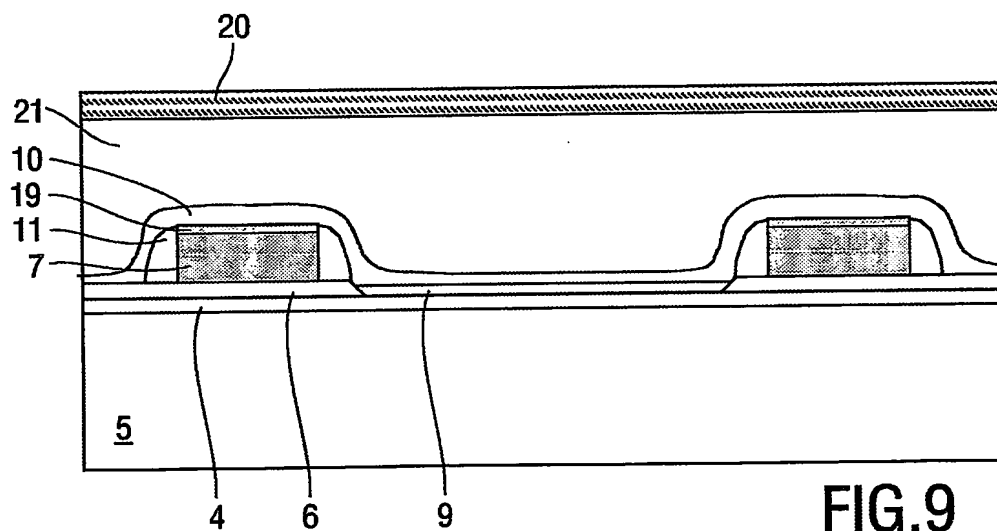


FIG. 9

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